

CBPR (RIGHT HAND) TABLE OF CONTENTS

PAGE	DESCRIPTION
CBPR.1	TABLE OF CONTENTS
CBPR.2-6	NMB CONNECTOR
CBPR.7-11	NSP CONNECTOR
CBPR.12-16	NVP CONNECTOR
CBPR.17	P11 CONNECTOR
CBPR.18	P12 CONNECTOR
CBPR.19	P13 CONNECTOR
CBPR.20	CLOCK CONNECTOR, COP CHIP, AND COP CONNECTOR
CBPR.21	SYNONYMS, SLOT ID

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TITLE: CBPR
DRAWING: 411-001287-300B REV 0.0

ABBR: CBPR
ENGR: GOLEMBIESKI

REVISED: Sun Oct 7 21:06:00 1990

PAGE: 1 OF 21

8

7

6

5

4

3

2

1

D

D

C

C

B

B

A

A

8

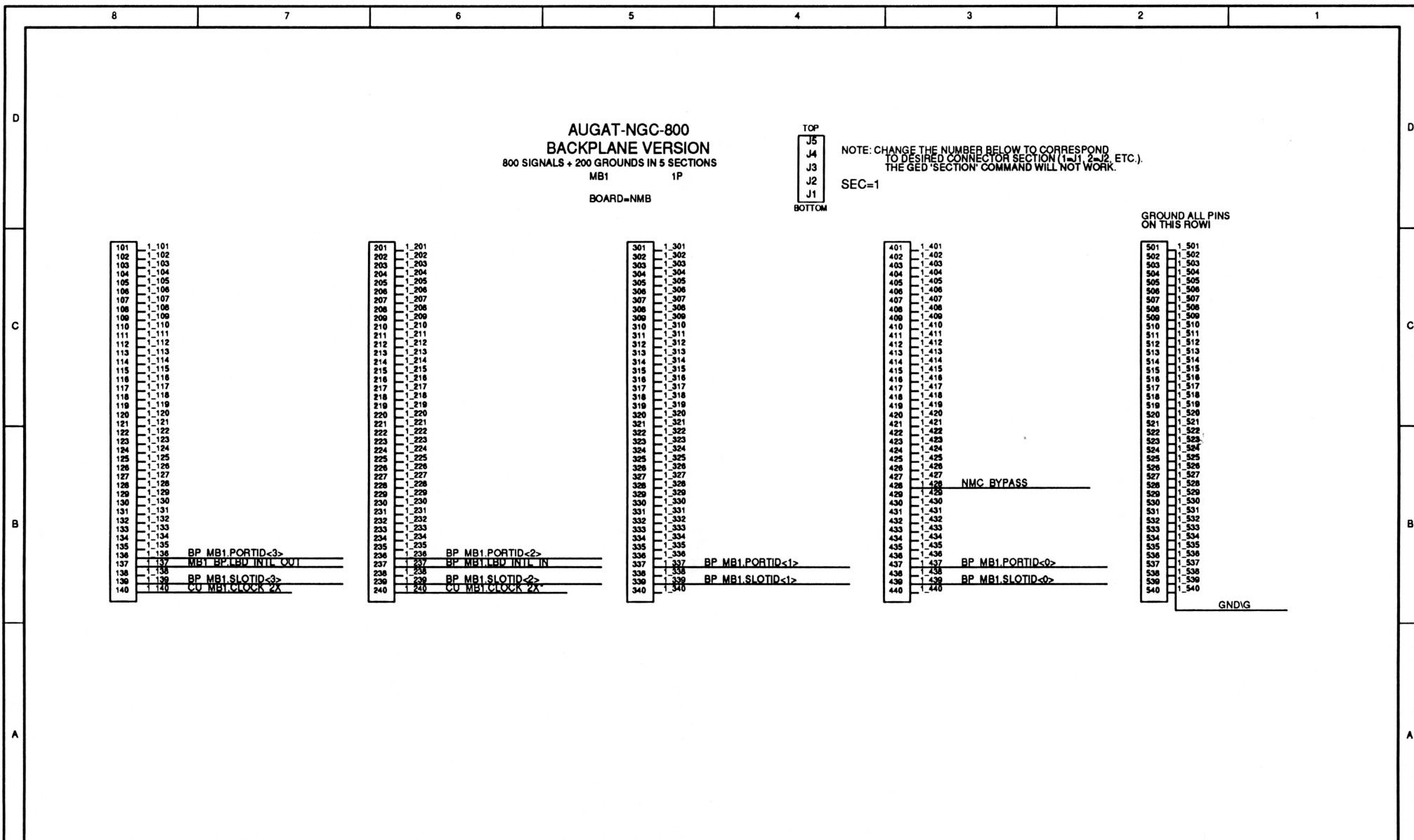
7

6

3

2

1



AUGAT-NGC-800
BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 MB1 1P
 BOARD=NMB

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

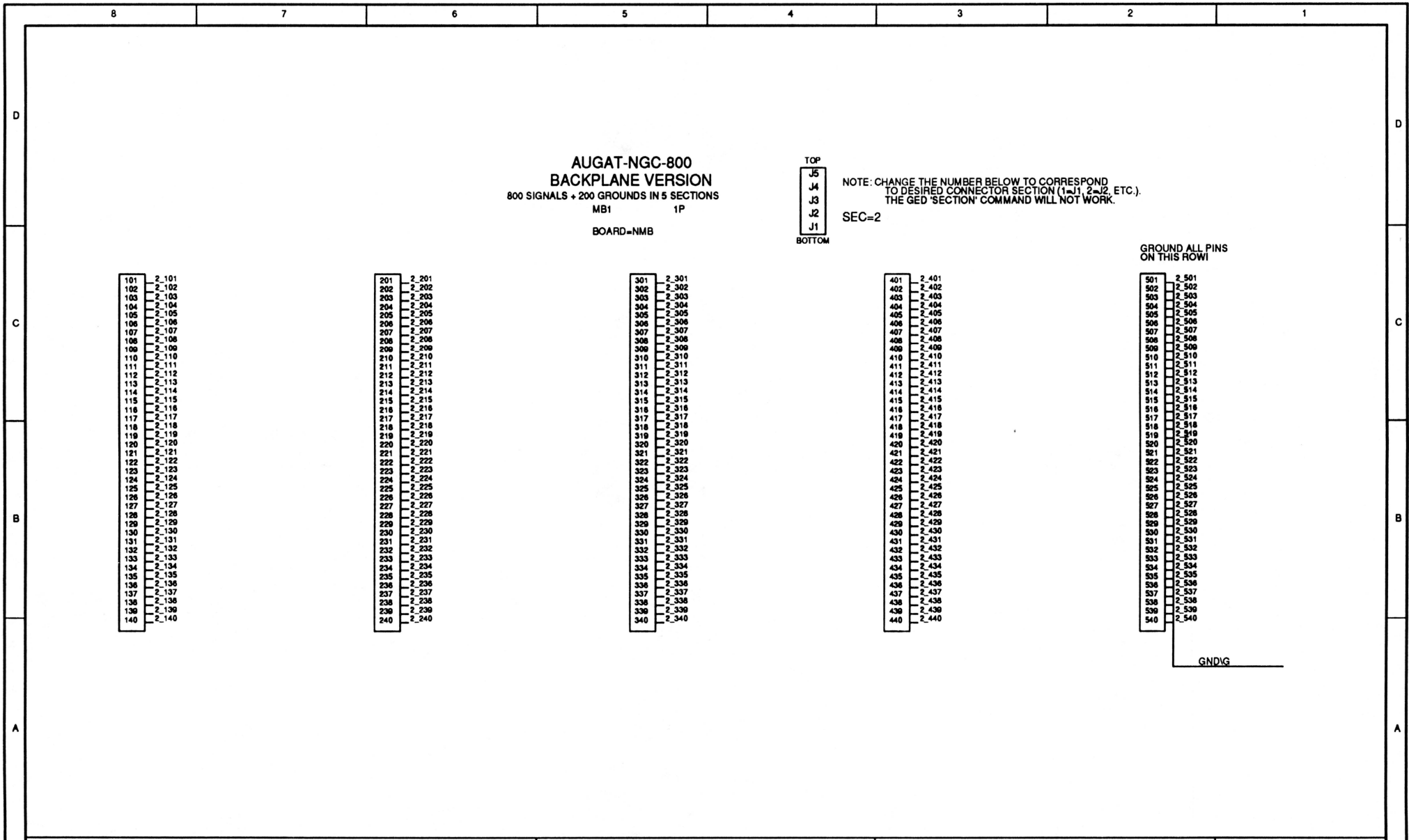
NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
 SEC=1

GROUND ALL PINS ON THIS ROW!

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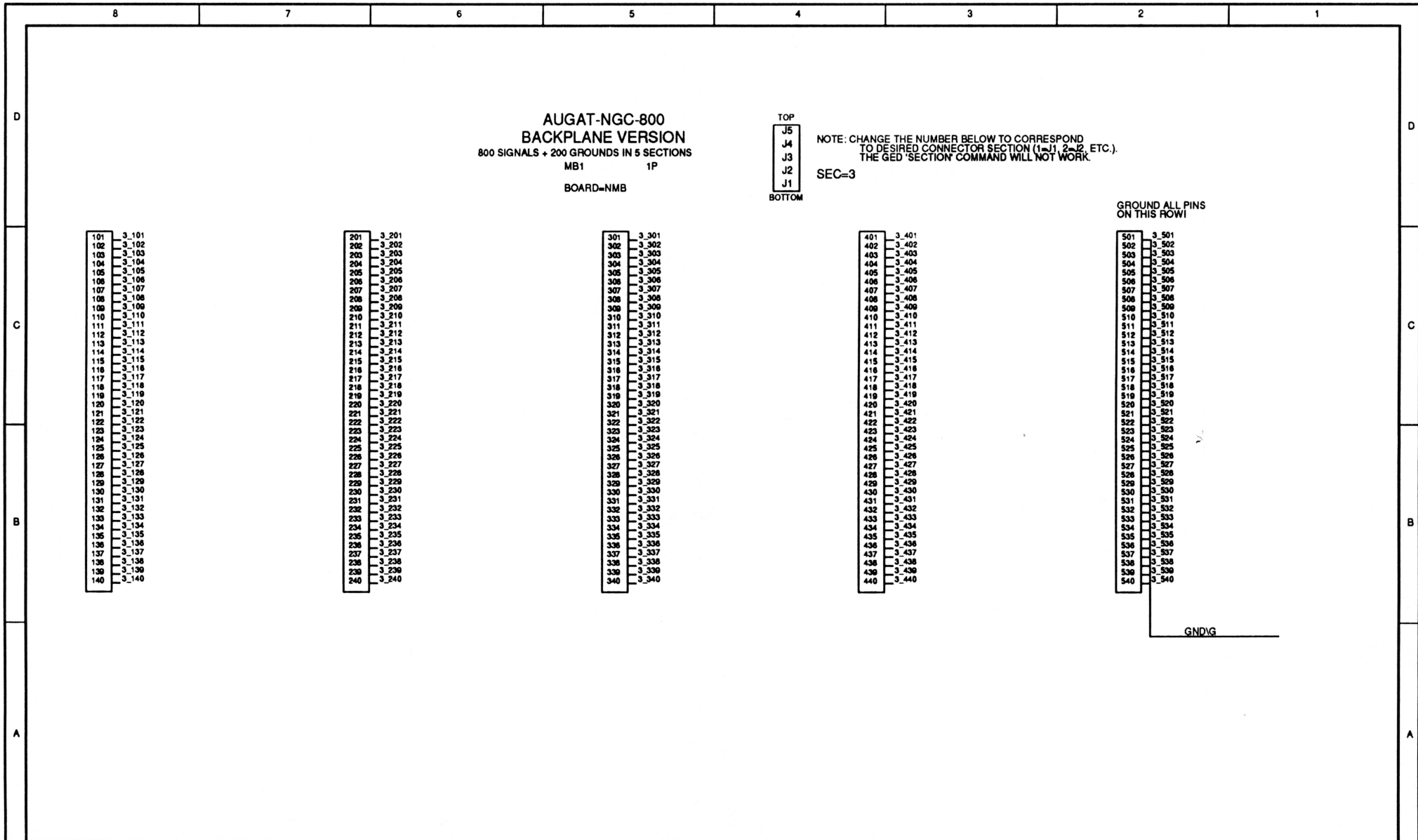
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REVISED: Sun Oct 7 21:07:08 1990	PAGE: 3 of 21	



**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
MB1 1P
BOARD=NMB

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND
TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=3

GROUND ALL PINS
ON THIS ROW!

GND/G

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PAGE: 4 of 21

8

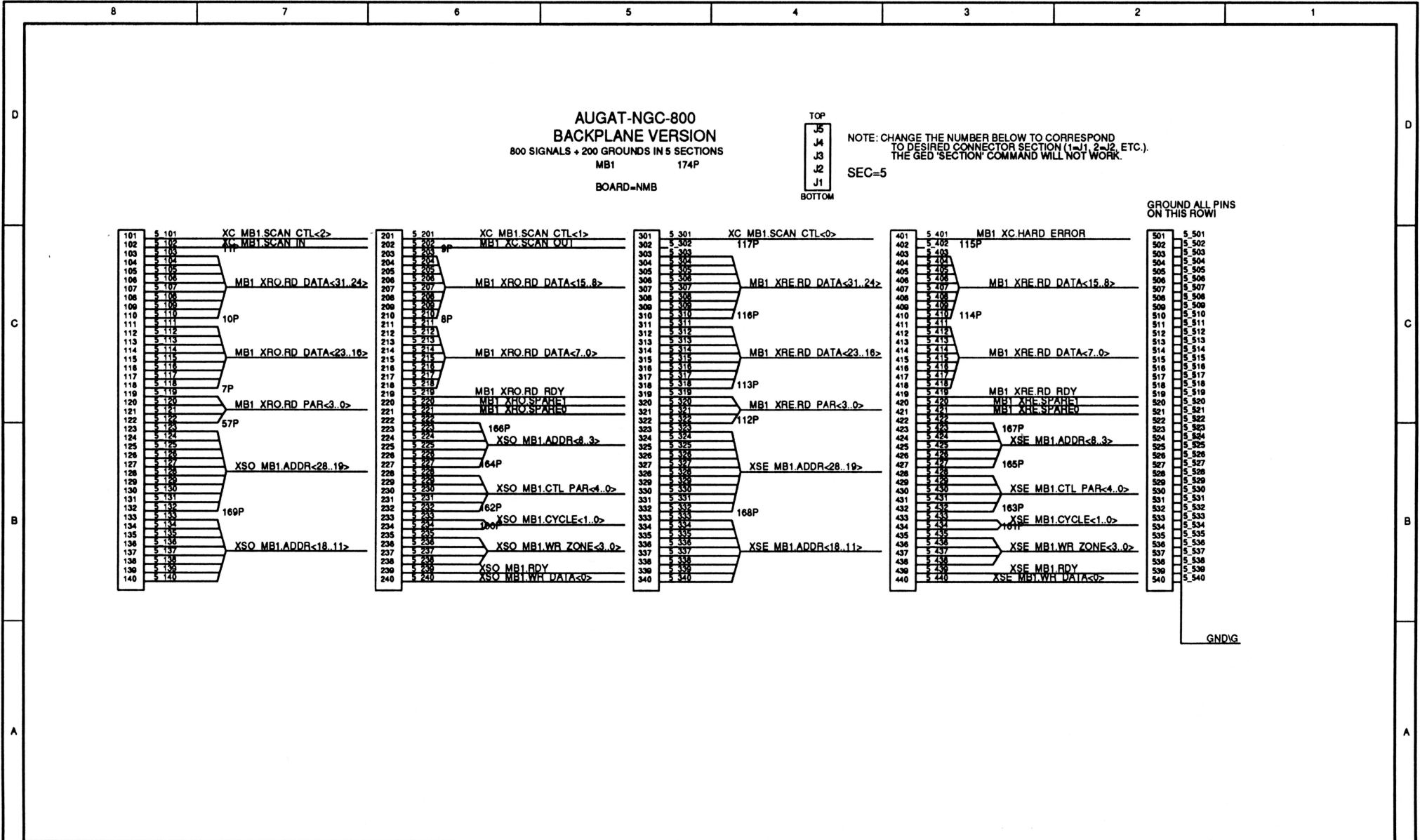
7

6

3

2

1



AUGAT-NGC-800
BACKPLANE VERSION
 800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
 MB1 174P
 BOARD=NMB

TOP
 J5
 J4
 J3
 J2
 J1
 BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND
 TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
 THE GED 'SECTION' COMMAND WILL NOT WORK.
 SEC=5

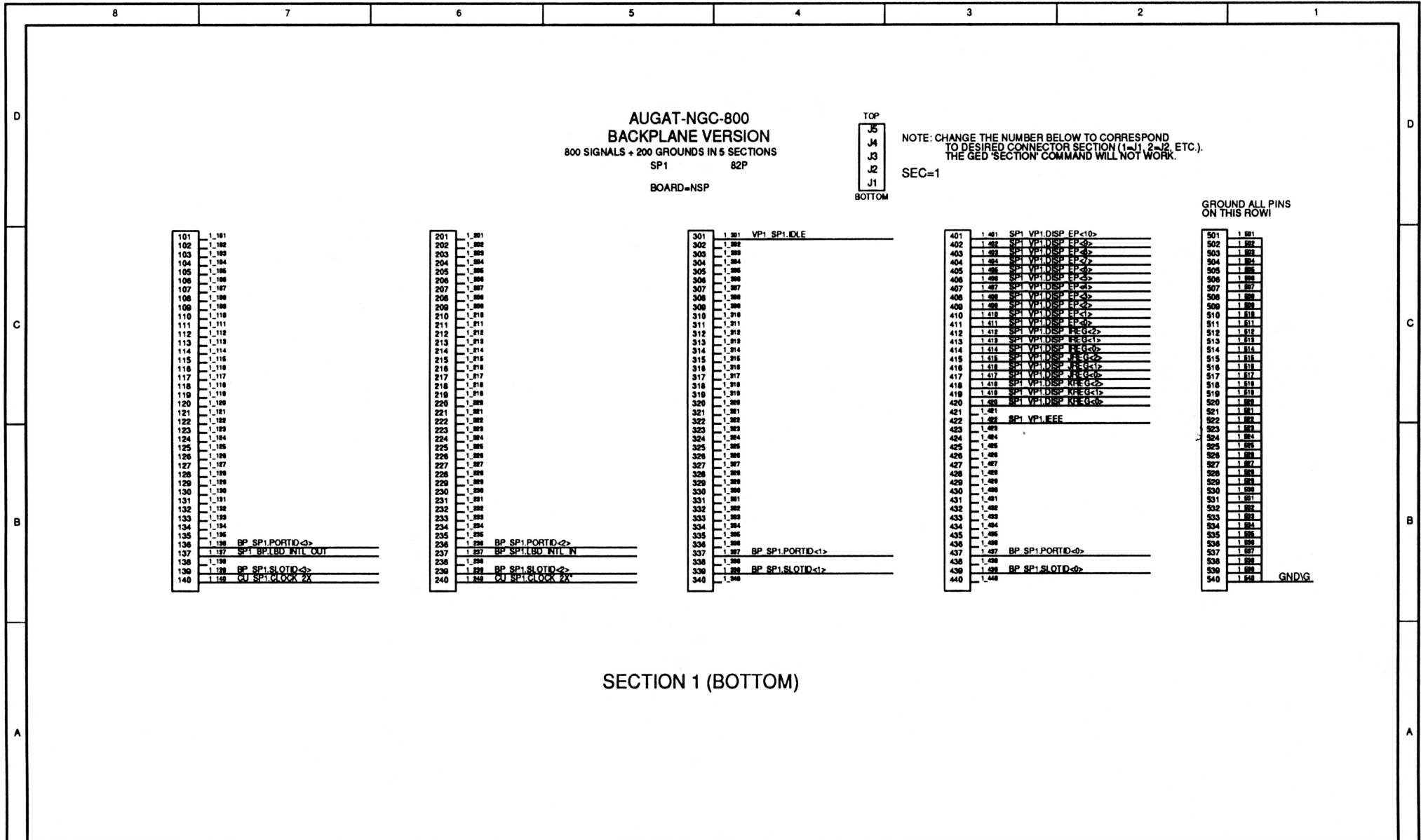
GROUND ALL PINS
ON THIS ROW!

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SECTION 1 (BOTTOM)

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REVISED: Sun Oct 7 21:10:58 1990	PAGE: 7 of 21

8 7 6 5 4 3 2 1

**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
SP1 90P
BOARD=NSP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=2

GROUND ALL PINS ON THIS ROW!

101	1 181	XRO SP1 RD DATA<5>
102	1 182	XRO SP1 RD DATA<6>
103	1 183	XRO SP1 RD DATA<7>
104	1 184	XRO SP1 RD DATA<13>
105	1 185	XRO SP1 RD DATA<14>
106	1 186	XRO SP1 RD DATA<15>
107	1 187	XRO SP1 RD DATA<21>
108	1 188	XRO SP1 RD DATA<22>
109	1 189	XRO SP1 RD DATA<23>
110	1 118	XRO SP1 RD DATA<28>
111	1 111	XRO SP1 RD DATA<30>
112	1 112	XRO SP1 RD DATA<31>
113	1 113	XRE SP1 RD DATA<5>
114	1 114	XRE SP1 RD DATA<6>
115	1 115	XRE SP1 RD DATA<7>
116	1 116	XRE SP1 RD DATA<13>
117	1 117	XRE SP1 RD DATA<14>
118	1 118	XRE SP1 RD DATA<15>
119	1 119	XRE SP1 RD DATA<21>
120	1 120	XRE SP1 RD DATA<22>
121	1 121	XRE SP1 RD DATA<23>
122	1 122	XRE SP1 RD DATA<28>
123	1 123	XRE SP1 RD DATA<30>
124	1 124	XRE SP1 RD DATA<31>
125	1 185	
126	1 186	
127	1 187	
128	1 188	
129	1 189	
130	1 190	
131	1 191	
132	1 192	
133	1 193	
134	1 194	
135	1 195	
136	1 196	
137	1 197	VP1 SP1 SPARE3
138	1 198	VP1 SP1 SPARE4
139	1 199	VP1 SP1 SPARE5
140	1 199	VP1 SP1 SPARE0

201	1 201	SP1 XSO WR DATA<5>
202	1 202	SP1 XSO WR DATA<6>
203	1 203	SP1 XSO WR DATA<7>
204	1 204	SP1 XSO WR DATA<13>
205	1 205	SP1 XSO WR DATA<14>
206	1 206	SP1 XSO WR DATA<15>
207	1 207	SP1 XSO WR DATA<21>
208	1 208	SP1 XSO WR DATA<22>
209	1 209	SP1 XSO WR DATA<23>
210	1 210	SP1 XSO WR DATA<28>
211	1 211	SP1 XSO WR DATA<30>
212	1 212	SP1 XSO WR DATA<31>
213	1 213	SP1 XSE WR DATA<5>
214	1 214	SP1 XSE WR DATA<6>
215	1 215	SP1 XSE WR DATA<7>
216	1 216	SP1 XSE WR DATA<13>
217	1 217	SP1 XSE WR DATA<14>
218	1 218	SP1 XSE WR DATA<15>
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222	1 222	SP1 XSE WR DATA<28>
223	1 223	SP1 XSE WR DATA<30>
224	1 224	SP1 XSE WR DATA<31>
225	1 225	
226	1 226	
227	1 227	
228	1 228	
229	1 229	
230	1 230	
231	1 231	
232	1 232	
233	1 233	
234	1 234	
235	1 235	
236	1 236	
237	1 237	
238	1 238	
239	1 239	
240	1 240	

301	1 301	VP1 SP1 DATA<5>
302	1 302	VP1 SP1 DATA<6>
303	1 303	VP1 SP1 DATA<7>
304	1 304	VP1 SP1 DATA<13>
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309	1 309	VP1 SP1 DATA<23>
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311	1 311	VP1 SP1 DATA<30>
312	1 312	VP1 SP1 DATA<31>
313	1 313	VP1 XSE DATA<5>
314	1 314	VP1 XSE DATA<6>
315	1 315	VP1 XSE DATA<7>
316	1 316	VP1 XSE DATA<13>
317	1 317	VP1 XSE DATA<14>
318	1 318	VP1 XSE DATA<15>
319	1 319	VP1 XSE DATA<21>
320	1 320	VP1 XSE DATA<22>
321	1 321	VP1 XSE DATA<23>
322	1 322	VP1 XSE DATA<28>
323	1 323	VP1 XSE DATA<30>
324	1 324	VP1 XSE DATA<31>
325	1 325	
326	1 326	
327	1 327	
328	1 328	
329	1 329	
330	1 330	VP1 SP1 MXV REQ NEXT
331	1 331	VP1 SP1 SXV REQ NEXT
332	1 332	
333	1 333	VP1 SP1 PSW HAZ
334	1 334	VP1 SP1 SET IDZ
335	1 335	VP1 SP1 SET PSN
336	1 336	VP1 SP1 SET OV
337	1 337	VP1 SP1 SET IRO
338	1 338	VP1 SP1 SET SIZ
339	1 339	VP1 SP1 SET UN
340	1 340	

401	1 401	SP1 VP1 DATA<5>
402	1 402	SP1 VP1 DATA<6>
403	1 403	SP1 VP1 DATA<7>
404	1 404	SP1 VP1 DATA<13>
405	1 405	SP1 VP1 DATA<14>
406	1 406	SP1 VP1 DATA<15>
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423	1 423	SP1 VP1 DATA<62>
424	1 424	SP1 VP1 DATA<63>
425	1 425	
426	1 426	
427	1 427	
428	1 428	
429	1 429	
430	1 430	
431	1 431	
432	1 432	SP1 VP1 DISP RDY
433	1 433	SP1 VP1 DISP PSW HAZ
434	1 434	
435	1 435	
436	1 436	
437	1 437	
438	1 438	
439	1 439	
440	1 440	

501	1 501	
502	1 502	
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540	1 540	GND/G

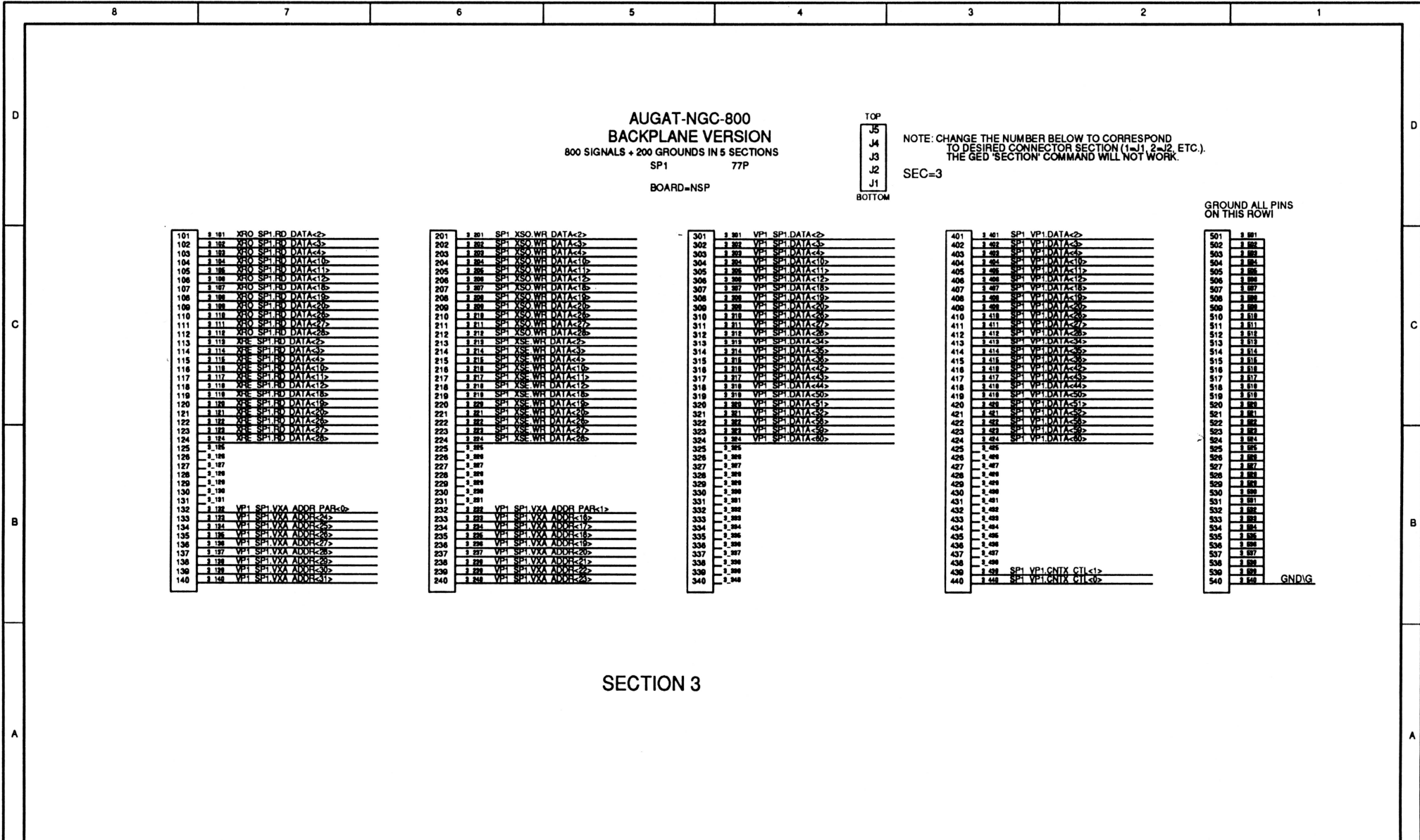
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8 7 6 3 2 1



SECTION 3

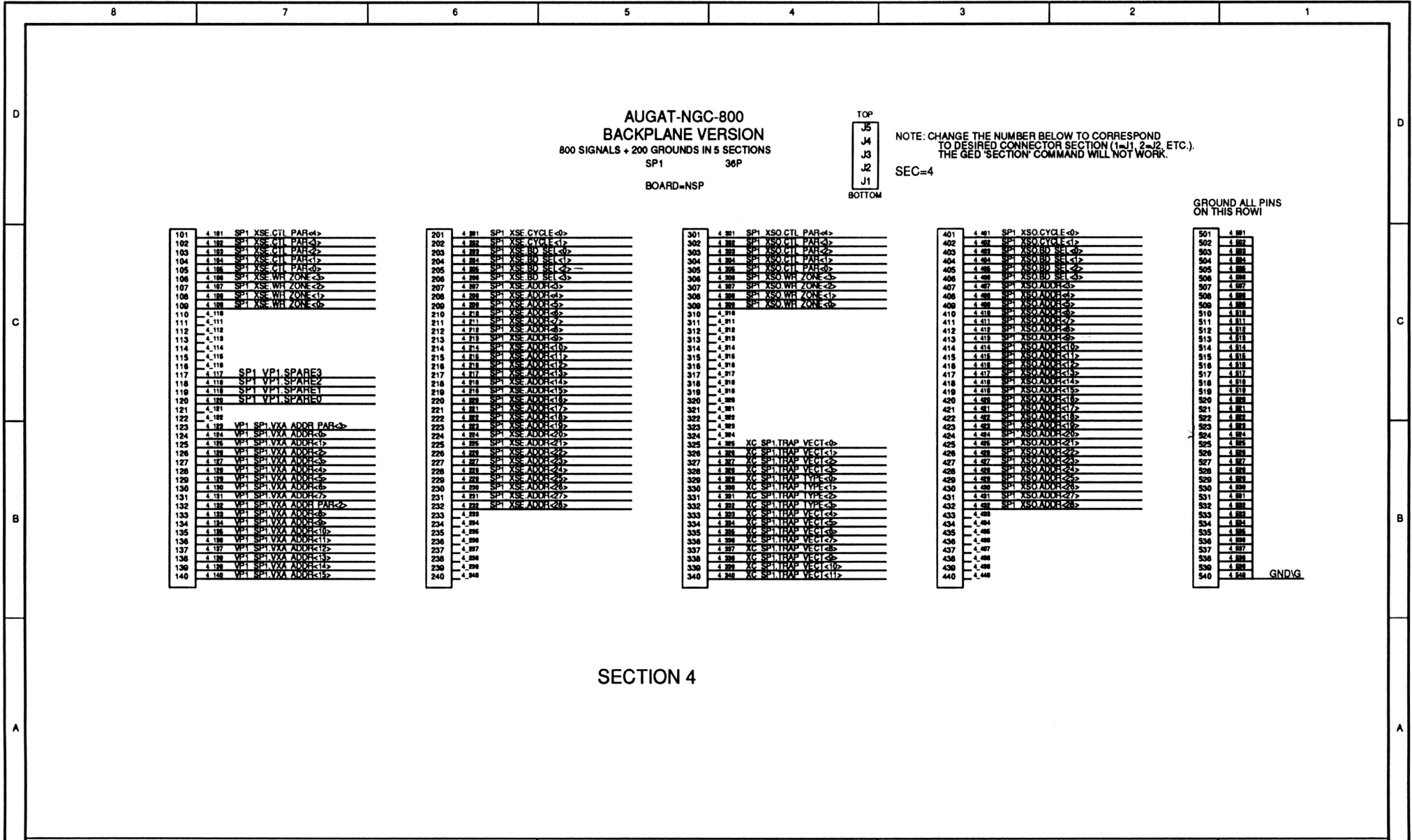
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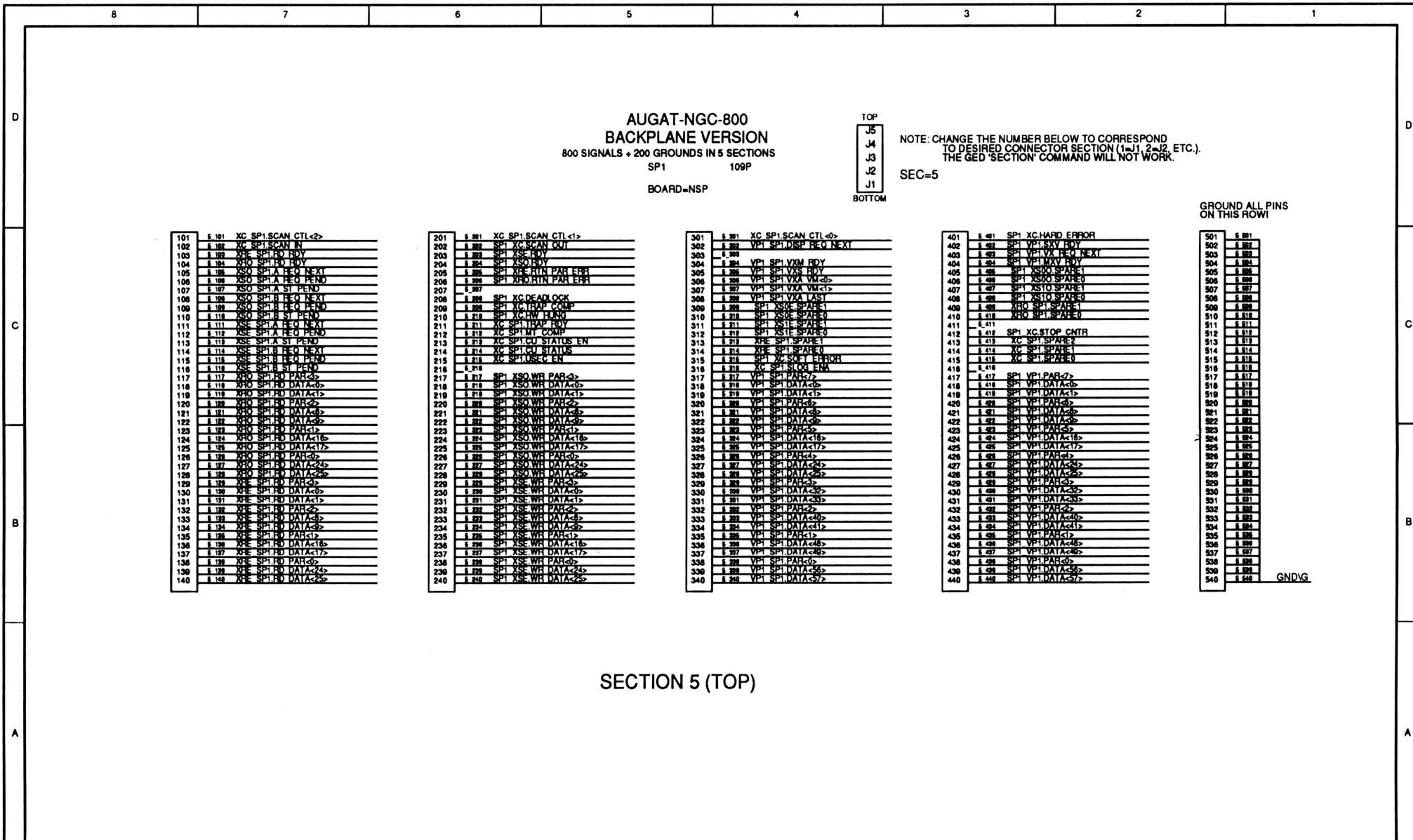




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SECTION 5 (TOP)

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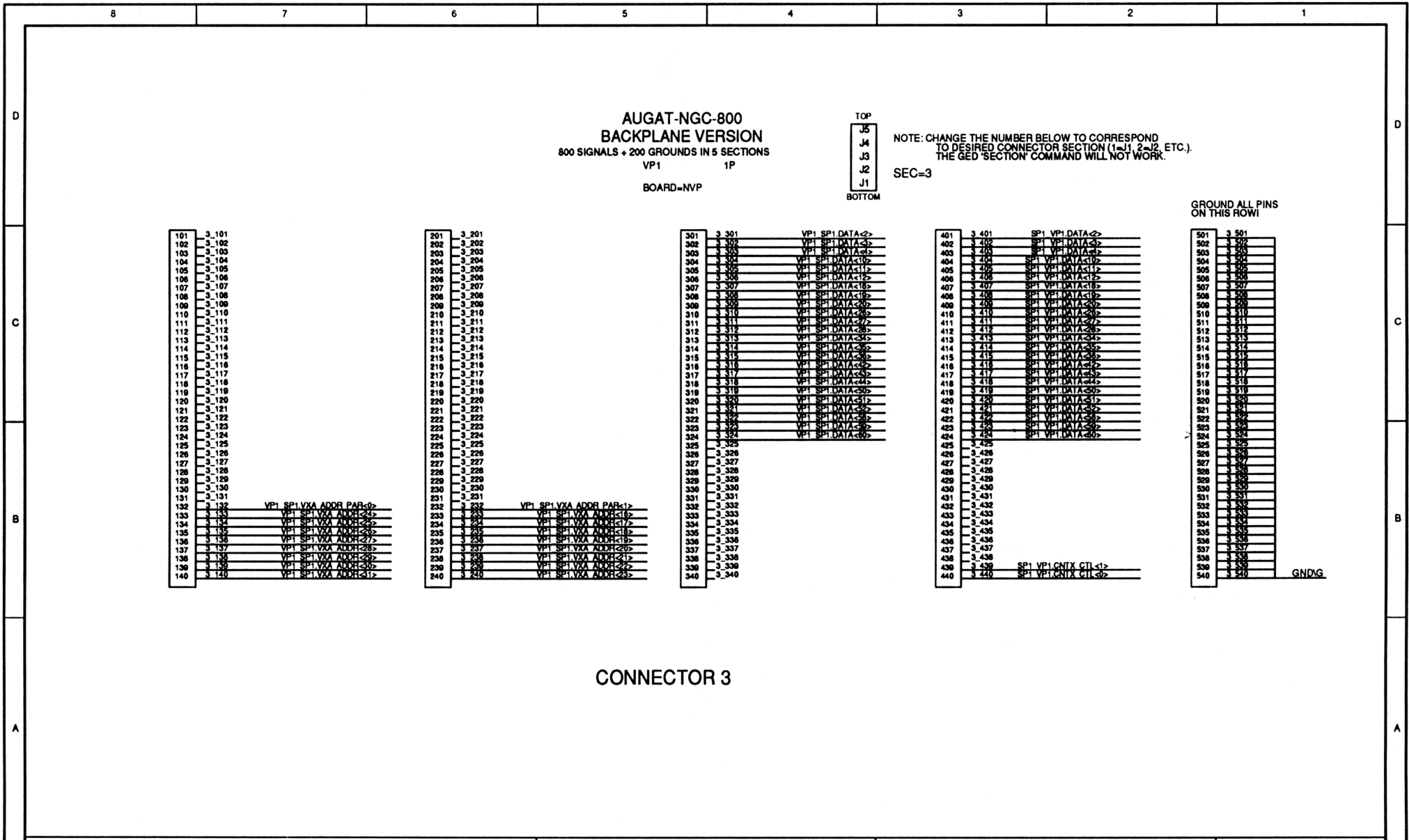


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PAGE: 11 of 21





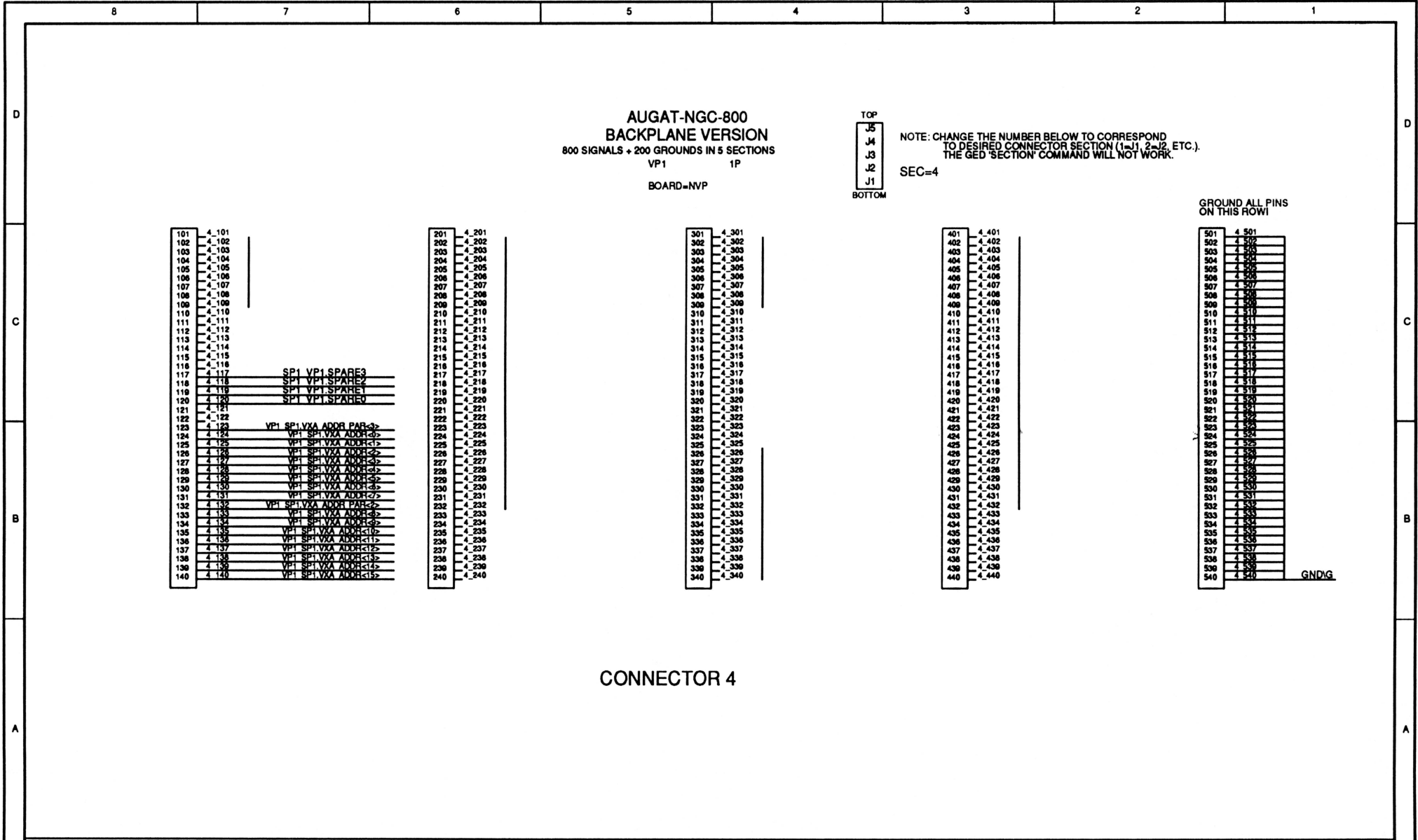
CONNECTOR 3

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**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
VP1 1P
BOARD=NVP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.). THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=4

GROUND ALL PINS ON THIS ROW!

101	4_101	
102	4_102	
103	4_103	
104	4_104	
105	4_105	
106	4_106	
107	4_107	
108	4_108	
109	4_109	
110	4_110	
111	4_111	
112	4_112	
113	4_113	
114	4_114	
115	4_115	
116	4_116	
117	4_117	SP1 VP1 SPARE3
118	4_118	SP1 VP1 SPARE2
119	4_119	SP1 VP1 SPARE1
120	4_120	SP1 VP1 SPARE0
121	4_121	
122	4_122	
123	4_123	VP1 SP1 VXA ADDR PAR<3>
124	4_124	VP1 SP1 VXA ADDR<3>
125	4_125	VP1 SP1 VXA ADDR<2>
126	4_126	VP1 SP1 VXA ADDR<1>
127	4_127	VP1 SP1 VXA ADDR<0>
128	4_128	VP1 SP1 VXA ADDR<3>
129	4_129	VP1 SP1 VXA ADDR<2>
130	4_130	VP1 SP1 VXA ADDR<1>
131	4_131	VP1 SP1 VXA ADDR<0>
132	4_132	VP1 SP1 VXA ADDR PAR<2>
133	4_133	VP1 SP1 VXA ADDR<2>
134	4_134	VP1 SP1 VXA ADDR<1>
135	4_135	VP1 SP1 VXA ADDR<0>
136	4_136	VP1 SP1 VXA ADDR<1>
137	4_137	VP1 SP1 VXA ADDR<0>
138	4_138	VP1 SP1 VXA ADDR<1>
139	4_139	VP1 SP1 VXA ADDR<0>
140	4_140	VP1 SP1 VXA ADDR<1>

201	4_201	
202	4_202	
203	4_203	
204	4_204	
205	4_205	
206	4_206	
207	4_207	
208	4_208	
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239	4_239	
240	4_240	

301	4_301	
302	4_302	
303	4_303	
304	4_304	
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340	4_340	

401	4_401	
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531	4_531	
532	4_532	
533	4_533	
534	4_534	
535	4_535	
536	4_536	
537	4_537	
538	4_538	
539	4_539	
540	4_540	GND/G

CONNECTOR 4

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ENGR: GOLEMBIESKI

PAGE: 15 of 21

8 7 6 5 4 3 2 1

**AUGAT-NGC-800
BACKPLANE VERSION**
800 SIGNALS + 200 GROUNDS IN 5 SECTIONS
VP1 1P
BOARD=NVP

TOP
J5
J4
J3
J2
J1
BOTTOM

NOTE: CHANGE THE NUMBER BELOW TO CORRESPOND TO DESIRED CONNECTOR SECTION (1=J1, 2=J2, ETC.).
THE GED 'SECTION' COMMAND WILL NOT WORK.
SEC=5

GROUND ALL PINS ON THIS ROW!

101	5 101	XC VP1.SCAN CTL<2>
102	5 102	XC VP1.SCAN IN
103	5 103	
104	5 104	
105	5 105	
106	5 106	
107	5 107	
108	5 108	
109	5 109	
110	5 110	
111	5 111	
112	5 112	
113	5 113	
114	5 114	
115	5 115	
116	5 116	
117	5 117	
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122	5 122	
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128	5 128	
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130	5 130	
131	5 131	
132	5 132	
133	5 133	
134	5 134	
135	5 135	
136	5 136	
137	5 137	
138	5 138	
139	5 139	
140	5 140	

201	5 201	XC VP1.SCAN CTL<1>
202	5 202	VP1 XC.SCAN OUT
203	5 203	
204	5 204	
205	5 205	
206	5 206	
207	5 207	
208	5 208	
209	5 209	
210	5 210	
211	5 211	
212	5 212	
213	5 213	
214	5 214	
215	5 215	
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301	5 301	XC VP1.SCAN CTL<0>
302	5 302	VP1 SPT.DSP REQ NEXT
303	5 303	
304	5 304	VP1 SPT.VXM RDY
305	5 305	VP1 SPT.VXS RDY
306	5 306	VP1 SPT.VXA VM<2>
307	5 307	VP1 SPT.VXA VM<1>
308	5 308	VP1 SPT.VXA LAST
309	5 309	
310	5 310	
311	5 311	
312	5 312	
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314	5 314	
315	5 315	
316	5 316	
317	5 317	VP1 SPT.PAR<2>
318	5 318	VP1 SPT.DATA<2>
319	5 319	VP1 SPT.DATA<1>
320	5 320	VP1 SPT.PAR<2>
321	5 321	VP1 SPT.DATA<2>
322	5 322	VP1 SPT.DATA<2>
323	5 323	VP1 SPT.PAR<2>
324	5 324	VP1 SPT.DATA<1>
325	5 325	VP1 SPT.PAR<1>
326	5 326	VP1 SPT.PAR<2>
327	5 327	VP1 SPT.DATA<2>
328	5 328	VP1 SPT.DATA<2>
329	5 329	VP1 SPT.PAR<2>
330	5 330	VP1 SPT.DATA<2>
331	5 331	VP1 SPT.DATA<2>
332	5 332	VP1 SPT.PAR<2>
333	5 333	VP1 SPT.DATA<2>
334	5 334	VP1 SPT.DATA<1>
335	5 335	VP1 SPT.PAR<1>
336	5 336	VP1 SPT.DATA<2>
337	5 337	VP1 SPT.DATA<2>
338	5 338	VP1 SPT.PAR<2>
339	5 339	VP1 SPT.DATA<2>
340	5 340	VP1 SPT.DATA<2>

401	5 401	VP1 XC.HARD ERROR
402	5 402	SPT VP1.SKV RDY
403	5 403	SPT VP1.VX REQ NEXT
404	5 404	SPT VP1.MXV RDY
405	5 405	
406	5 406	
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415	5 415	
416	5 416	
417	5 417	SPT VP1.PAR<2>
418	5 418	SPT VP1.DATA<2>
419	5 419	SPT VP1.DATA<1>
420	5 420	SPT VP1.PAR<2>
421	5 421	SPT VP1.DATA<2>
422	5 422	SPT VP1.DATA<2>
423	5 423	SPT VP1.PAR<2>
424	5 424	SPT VP1.DATA<1>
425	5 425	SPT VP1.DATA<1>
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427	5 427	SPT VP1.DATA<2>
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431	5 431	SPT VP1.DATA<2>
432	5 432	SPT VP1.PAR<2>
433	5 433	SPT VP1.DATA<2>
434	5 434	SPT VP1.DATA<1>
435	5 435	SPT VP1.PAR<1>
436	5 436	SPT VP1.DATA<2>
437	5 437	SPT VP1.DATA<2>
438	5 438	SPT VP1.PAR<2>
439	5 439	SPT VP1.DATA<2>
440	5 440	SPT VP1.DATA<2>

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540	5 540	GND/G

CONNECTOR 5 (TOP)

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PAGE: 16 of 21

8 7 6 3 2 1



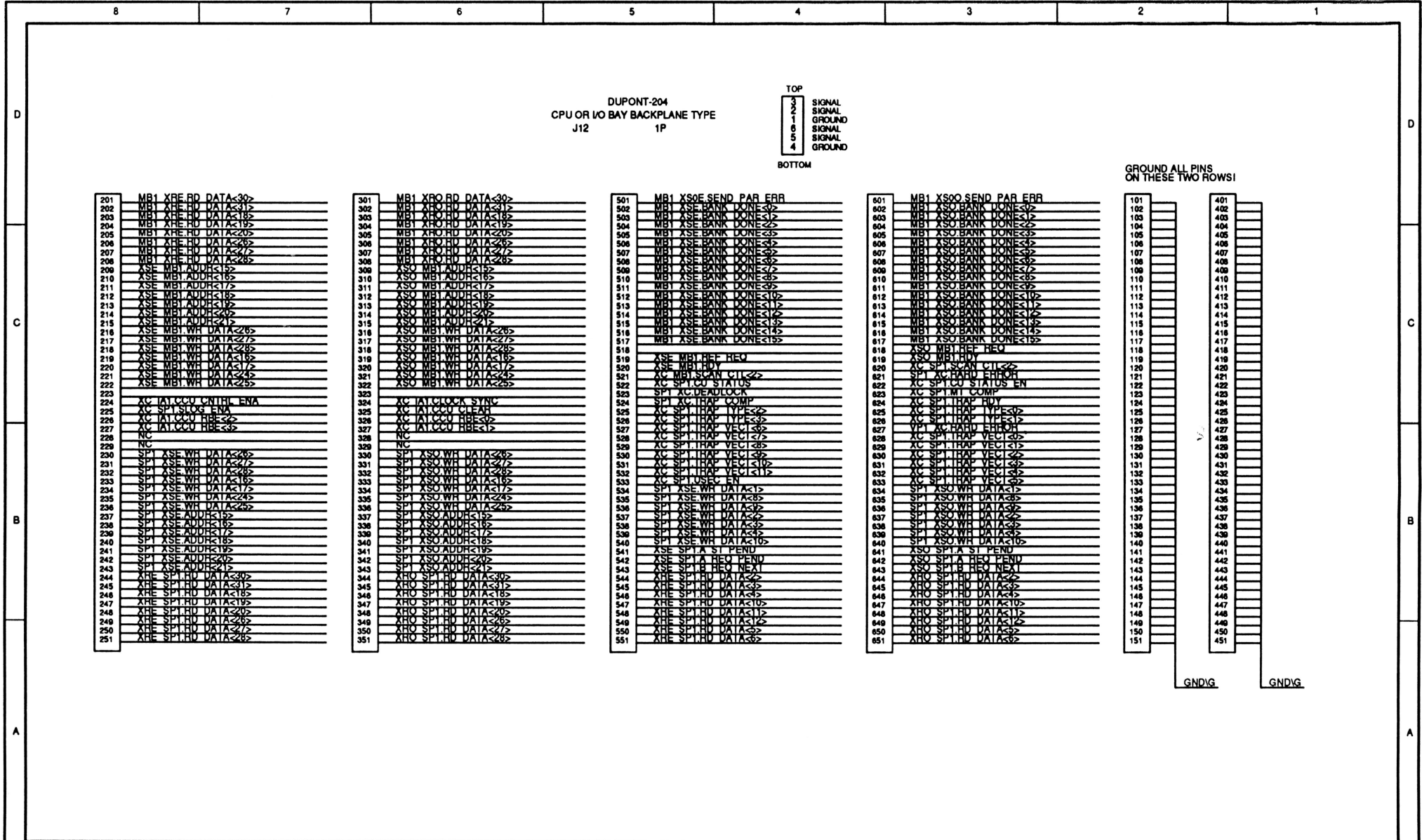
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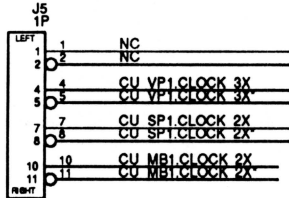


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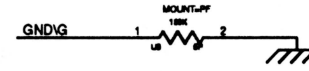
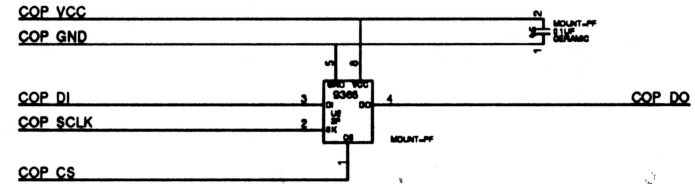
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PAGE: 18 OF 21

C3 CLOCK CONNECTOR
12 PIN STRAIGHT, 3X4 ROW
(AS VIEWED FROM INSIDE CARDCAGE)



HDR10
301-000035-004
MALE-PRESSFIT-STRAIGHT-4 WALL-LONG EJECTOR



RESISTOR CONNECTS CHASSIS GND TO SIGNAL GND

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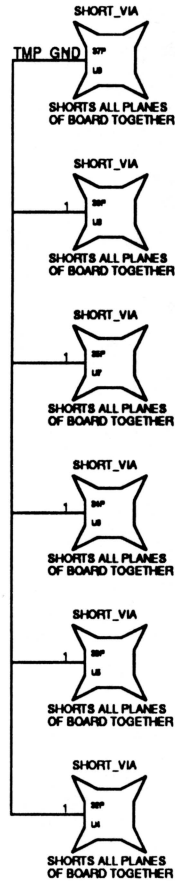
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PAGE: 20 of 21

SYNONYMS

BP BP.PORTID <3>	12P	BP MB1.PORTID<3>
BP BP.PORTID <2>	11P	BP MB1.PORTID<2>
BP BP.PORTID <1>	10P	BP MB1.PORTID<1>
BP BP.PORTID <0>	9P	BP MB1.PORTID<0>
BP BP.PORTID <3>	8P	BP SP1.PORTID<3>
BP BP.PORTID <2>	7P	BP SP1.PORTID<2>
BP BP.PORTID <1>	6P	BP SP1.PORTID<1>
BP BP.PORTID <0>	5P	BP SP1.PORTID<0>
BP BP.PORTID <3>	4P	BP VP1.PORTID<3>
BP BP.PORTID <2>	3P	BP VP1.PORTID<2>
BP BP.PORTID <1>	2P	BP VP1.PORTID<1>
BP BP.PORTID <0>	1P	BP VP1.PORTID<0>
MB1 BP.LBD INTL OUT	29P	BP MB1.LBD INTL IN
SP1 BP.LBD INTL OUT	30P	BP SP1.LBD INTL IN
VP1 BP.LBD INTL OUT	31P	BP VP1.LBD INTL IN



NOTE: THESE VIAS ARE A TEMPORARY FIX TO FIX A BUG IN THE REV A ARTWORK. THEY CAN BE REMOVED ON LATER VERSIONS OF THE ARTWORK. THEY TIE THE APPROPRIATE SLOTIDS TO GNDG

BPTYPE<3>	21P	COP_GND
BPTYPE<2>	22P	COP_GND
BPTYPE<1>	23P	NC
BPTYPE<0>	24P	NC
CPU RIGHT BPTYPE = 0X3		
BP MB1.SLOTID<3>	25P	NC
BP MB1.SLOTID<2>	26P	GNDG
BP MB1.SLOTID<1>	27P	NC
BP MB1.SLOTID<0>	28P	NC
MB1 SLOTID = 0XB		
BP SP1.SLOTID<3>	17P	NC
BP SP1.SLOTID<2>	18P	GNDG
BP SP1.SLOTID<1>	19P	NC
BP SP1.SLOTID<0>	20P	GNDG
SP1 SLOTID = 0XA		
BP VP1.SLOTID<3>	13P	NC
BP VP1.SLOTID<2>	14P	GNDG
BP VP1.SLOTID<1>	15P	GNDG
BP VP1.SLOTID<0>	16P	NC
VP1 SLOTID = 0X9		

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